






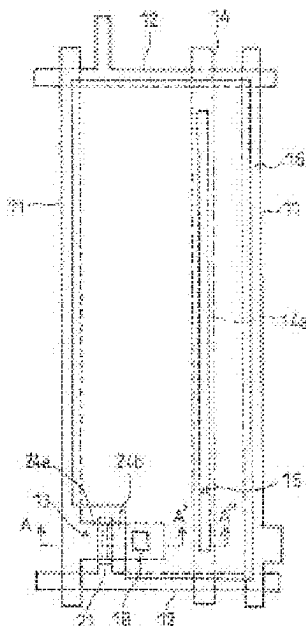
**ACTIVE MATRIX SUBSTRATE, ITS MANUFACTURE, AND IMAGE SENSOR USING THE SAME****Publication number:** JP2000323698 (A)**Publication date:** 2000-11-24**Inventor(s):** NAGATA HISASHI; IZUMI YOSHIHIRO; SHIMADA NAOYUKI**Applicant(s):** SHARP KK**Classification:**

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- European: G02F1/1362C; H01L27/146A4

**Application number:** JP20000045034 20000222**Priority number(s):** JP20000045034 20000222; JP19990065520 19990311**Also published as:** JP3683463 (B2) EP1037095 (A2) EP1037095 (A3) US6784949 (B1) TW258626 (B)**Abstract of JP 2000323698 (A)**

**PROBLEM TO BE SOLVED:** To manufacture an active matrix substrate without increasing the number of steps which can prevent the delay of signal transmission in a signal line as well as the generation of crosstalk between pixels, and to provide an image sensor using the active matrix substrate. **SOLUTION:** A pixel capacitor wiring 14 common to a pixel capacitor electrode and a signal line 11 are formed parallel to each other by patterning the same electrode layer. Therefore, no additional step is required for the formation of the pixel capacitor wiring 14. In addition, due to such a configuration, the delay in signal transmission in the signal line and generation of crosstalk between pixels can be prevented because the pixel capacitor wiring 14 and the signal line 11 are parallel to each other. This active matrix substrate can be preferably used as an active matrix substrate for a liquid crystal display device or an image sensor, etc., for example.



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